

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		10/622,607	CHEN ET AL.	
Examiner		Art Unit		Page 1 of 1
Matthew W. Such		2891		

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-6,291,315	09-2001	Nakayama et al.	438/459
	C	US-			
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	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
*	U	Wolf S. & Tauber, R.N. SILICON PROCESSING FOR THE VLSI ERA: VOLUME 1 - PROCESS TECHNOLOGY. Lattice Press: Sunset Beach (1986): pages 418-423.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)

Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.